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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: October 22, 2003

Robert Thomas BAILIS

Confirmation No. 3353

Serial No: 10/016,772

Group Art Unit: 2819

Filed: December 10, 2001

Examiner: Chang, Daniel D.

METHOD AND SYSTEM FOR USE OF AN EMBEDDED FIELD PROGRAMMABLE GATE ARRAY INTERCONNECT FOR FLEXIBLE I/O

CONNECTIVITY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action dated July 22, 2003, please amend the above-identified application in the following manner:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.